

Memorandum Of Understanding Declaration of Graz 2025

THIS AGREEMENT is entered into force on [08.10.2025] between:

Silicon Alps Cluster GmbH, having its registered office in Villach, Europastrasse 12, A-9524 Villach/Magdalen, Austria – and represented by its CEO DI Dr. techn. Robert Gfrerer and,

[aCCcess], having its registered office in 38 rue Mermoz, 75008 Paris, France – and represented by its Legal Representative, BLUMORPHO. Acting as a representative of the aCCcess consortium, aCCcess is funded under the Chips Joint Undertaking Initiative under the contract number 101217840.

[AT-C³], having its registered office in Inffeldgasse 33, 8010 Graz, Austria, – and represented by its Legal Representative, Silicon Alps Cluster GmbH. AT-C³ is co-funded under the Chips Joint Undertaking Initiative under the contract number 101217532 and the Österreichische Forschungsförderungsgesellschaft mbH (FFG) under the contract number FO999917171.

[SCCC], having its registered office in Lunds Universitet (“LU”)], registration no. 202100-3211, having its registered office in P. O. Box 117, SE-221 00 Lund, Sweden. SCCC is co-funded under the Chips Joint Undertaking Initiative under the contract number 2024-04006 and Vinnova, Sweden’s Innovation Agency under the contract number SEK 29 967 220.

[CSC], having its registered office in Kolejní 3093/7, 612 00 Brno-Královo Pole, Czech Republic – and represented by its Legal Representative, Karel Masařík.

[Czech National Semiconductor Cluster - CNSC], having its registered office in Šumavská 15, 60200 Brno, CZ – and represented by its Legal Representative, Stanislav Černý, president.

[ASTEERICS], having its registered office in c/o CEA Grenoble – 29, rue Félix Esclançon, 38054 Grenoble Cedex 9, France – and represented by its Legal Representative, Minalogic.

[Chip NL Competence Center – CCC-NL], having its registered office in Achtseweg Zuid 159-H, 5651 GW Eindhoven, Netherland – and represented by its Legal Representative, Brainport Development.

Individually referred to as a “Party” or collectively as the “Parties”.

PREAMBLE

The undersigned parties, representing a group of **European Chips Competence Centres**,

- Recognising the strategic importance of semiconductors and microelectronics for Europe's technological sovereignty, economic resilience, and competitiveness in the global digital economy;
- Acknowledging the objectives of the European Chips Act and the "Chips for Europe Initiative" to strengthen Europe's capacity in semiconductor research, development, design, and production;
- Emphasising the need for close cooperation, knowledge exchange, and shared access to infrastructure, in order to accelerate innovation and foster a robust European semiconductor ecosystem;
- Affirming their commitment to openness, transparency, and non-discrimination, ensuring that SMEs, start-ups, academia, and industry at large benefit from the services of the Competence Centres;
- Determining to contribute to education, training, and skills development in order to strengthen Europe's workforce and attract talent to the semiconductor sector;
- Striving to enhance Europe's visibility, capacity for innovation, and ability to lead in emerging technologies such as
 - Thin film Technologies, Advanced Materials, Quantum & Photonics;
 - please PARTNERS add your core fields of expertise

hereby express their shared intention to cooperate within the framework of this Memorandum of Understanding, to foster collaboration across borders, and to jointly contribute to Europe's sustainable semiconductor ecosystem.

PURPOSE:

The Parties agree to put their best efforts to achieve the following objectives:

- Avoiding fragmentation and fostering synergies
The aCCcess Consortium will strive to coordinate all activities, acting as a central platform for networking, transparency, and governance. Together, they will strive to develop Europe-wide roadmaps, harmonize standards, and establish regular exchange formats to prevent duplication and build a coherent European semiconductor ecosystem.
- Ensuring fair and non-discriminatory access
The centres will strive for open access under the conditions given by their individual funding contracts to infrastructure, expertise, and funding for all relevant stakeholders, particularly SMEs and start-ups.
- Maximizing efficiency and impact of resources
Supported by the aCCcess Consortium, the centres will implement common practices for monitoring, evaluation, and best-practices sharing to ensure effective use of funding and infrastructure.
- Promoting skills development and training across Europe

SILICONALPS

In close cooperation with the aCCcess Consortium, the centres will coordinate educational and qualification programs, facilitate talent mobility across Europe, and actively address the semiconductor skills gap.

- Strengthening Europe's strategic independence
With the involvement of the aCCcess Consortium, the centres will strive to develop regional and European value chains, pilot facilities, and key technologies to enhance Europe's resilience in the semiconductor sector.
- Enhancing trust and visibility
The aCCcess Consortium will support the centres in transparent communication, reporting on achievements, and conducting outreach initiatives to increase the attractiveness and recognition of the European semiconductor sector.

EXCLUSIVITY:

The Parties do not grant exclusivity to each other on the basis of this MoU.

MoU NON-BINDING:

The Parties acknowledge that this MoU is of a non-binding nature, and that it does not create any legal or financial obligations for or between Parties whatsoever. It is recognised that nothing in this MoU authorises or empowers any of the Parties to speak or act on behalf of the other Parties.

EXPENSES:

Each Party will bear its own costs and expenses related to the actions or transactions contemplated by this MoU.

EFFECTIVE DATE AND TERMINATION:

This MoU is entered into as of the date it is signed by the later Party to sign ('Effective Date'). Each Party shall have the right to terminate this MoU in writing. This MoU will terminate automatically by 31st December 2028. No Party shall have any obligation or liability on any account whatsoever to the other Party.

CHANGES AND AMENDMENTS:

Changes and amendments to this MoU must be made in writing and signed by authorised representatives of the Parties.

SILICONALPS

Through this declaration, the Competence Centres, together with the aCCcess Consortium, reaffirm their commitment to building a strong, open, and future-oriented semiconductor ecosystem in Europe, thereby contributing significantly to Europe's technological sovereignty and global competitiveness.

SILICONALPS

[aCCcess], having its registered office in 38 rue Mermoz, 75008 Paris, France, – and represented by its Legal Representative, BLUMORPHO.

Date:

Stamp/Signature:

Régis Hamelin I CTO BLUMORPHO and

[AT-C3], having its registered office in Inffeldgasse 33, 8010 Graz, Austria – and represented by its Legal Representative, Silicon Alps Cluster GmbH.

Date:

Stamp/Signature:

Robert Gfrerer | CEO Silicon Alps Cluster GmbH

[SCCC], having its registered office in Lunds universitet ("LU")), registration no. 202100-3211, having its registered office in P. O. Box 117, SE-221 00 Lund, Sweden, – and represented by its Legal Representative, Lunds Universitet.

Date:

Stamp/Signature:

Magnus Nilsson | Head of Division Research,
Collaboration and Innovation, Lund University

[CSC], having its registered office in Kolejní 3093/7, 612 00 Brno-Královo Pole, Czech Republic,
– and represented by its Legal Representative, Karel Masařík.

Date:

Stamp/Signature:

Karel Masařík | Director CSC

SILICONALPS

[Czech National Semiconductor Cluster (CNSC)], having its registered office in Šumavská 15, 60200 Brno, CZ – and represented by its Legal Representative, Stanislav Černý, president.

Date:

Stamp/Signature:

Stanislav Černý | President

[ASTEERICS], having its registered office in c/o CEA Grenoble – 29, rue Félix Esclangon, 38054 Grenoble Cedex 9, France, – and represented by its Legal Representative, Minalogic.

Date:

Stamp/Signature:

Hervé Ribot | Director ASTEERICS

[Chip NL Competence Center – CCC-NL], having its registered office in Achtseweg Zuid 159-H, 5651 GW Eindhoven, Netherland, – and represented by its Legal Representative, Brainport Development.

Date:

Stamp/Signature:

Paul van Nunen | Managing Director

Brainport Development